

C4

FIG. 1

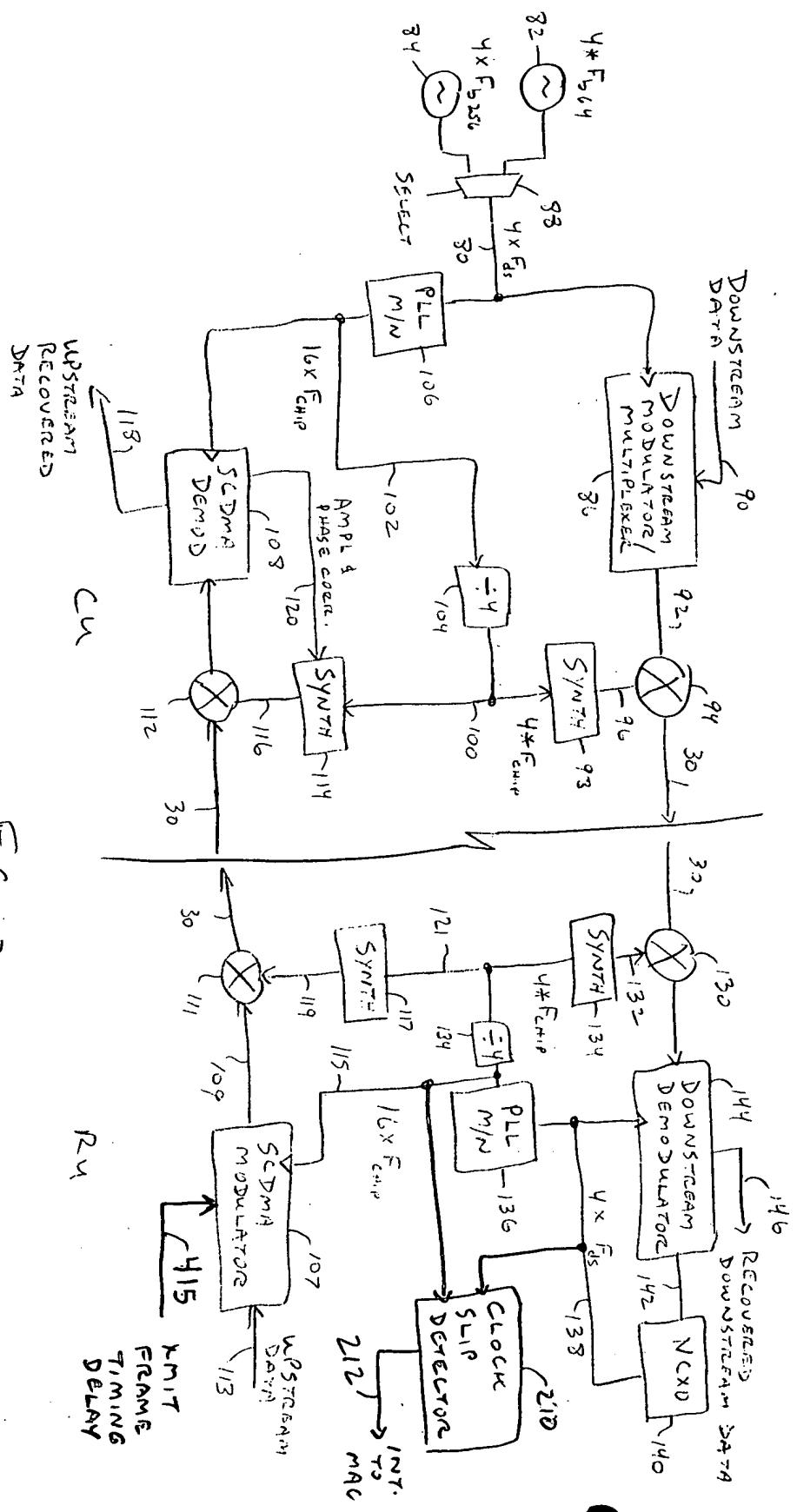


FIG. 2

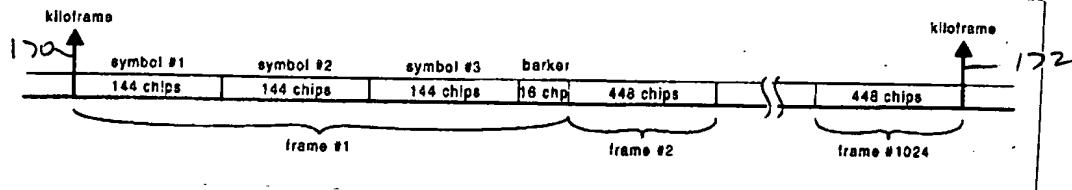


FIG. 3

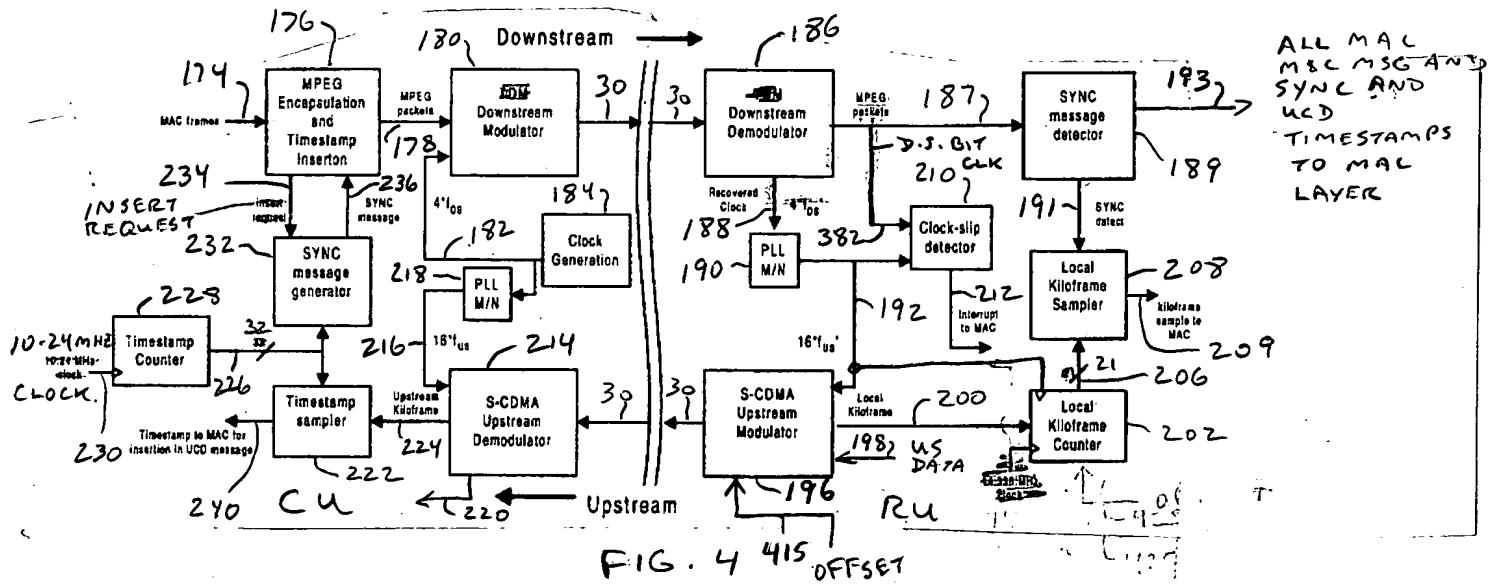
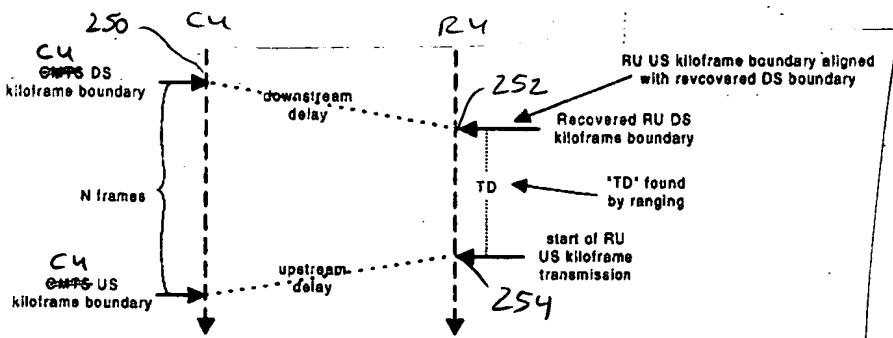


FIG. 4 415' OFFSET



SCDMA DS, SCDMA US  
FIG. 5

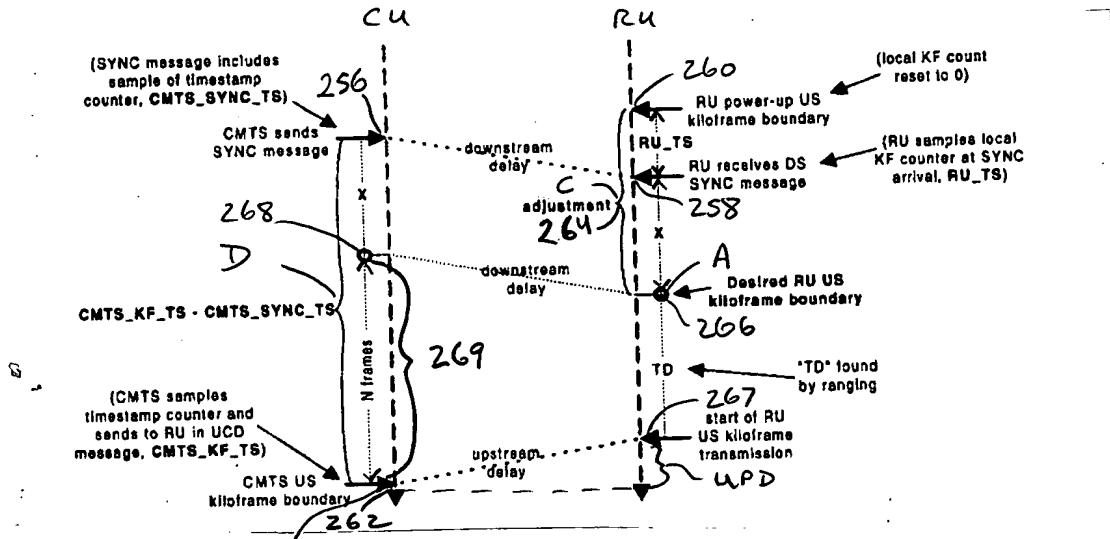


FIG. 6

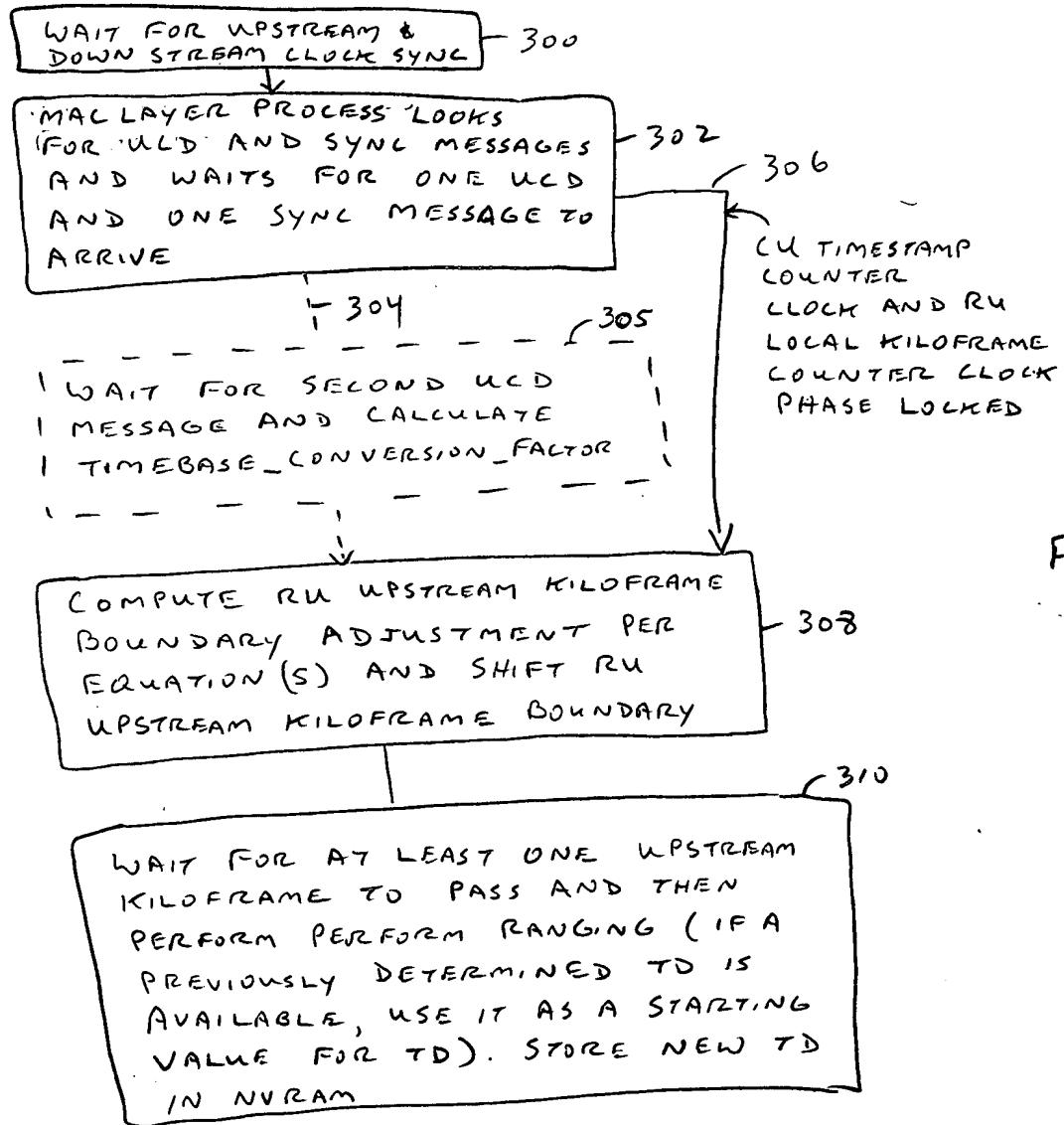


FIG. 7

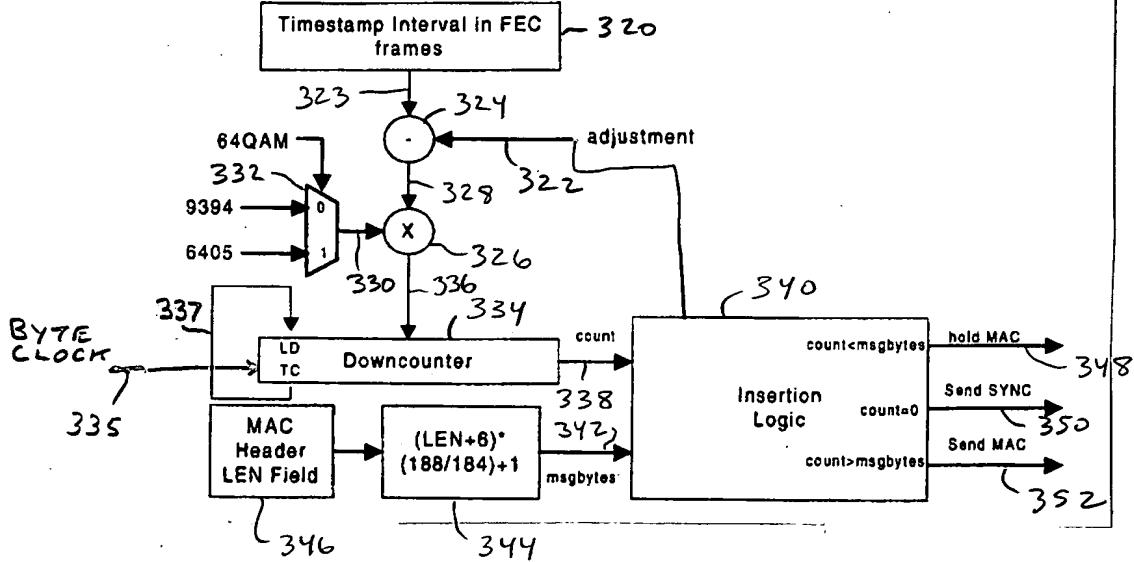


FIG. 8

**Table 1 64 QAM SYNC Start Position Adjustments**

SYNC Start Position in Bytes	SYNC Adjustment in FEC frames
0-5	2
155-167	4
167-183	2

FIG. 9

**Table 2 256 QAM SYNC Start Position Adjustments**

SYNC Start Position in Bytes	SYNC Adjustment in FEC frames
0-2	6
3-5	7
155-160	1
161-166	2
167-172	3
173-178	4
179-184	5
185-187	6

FIG. 10

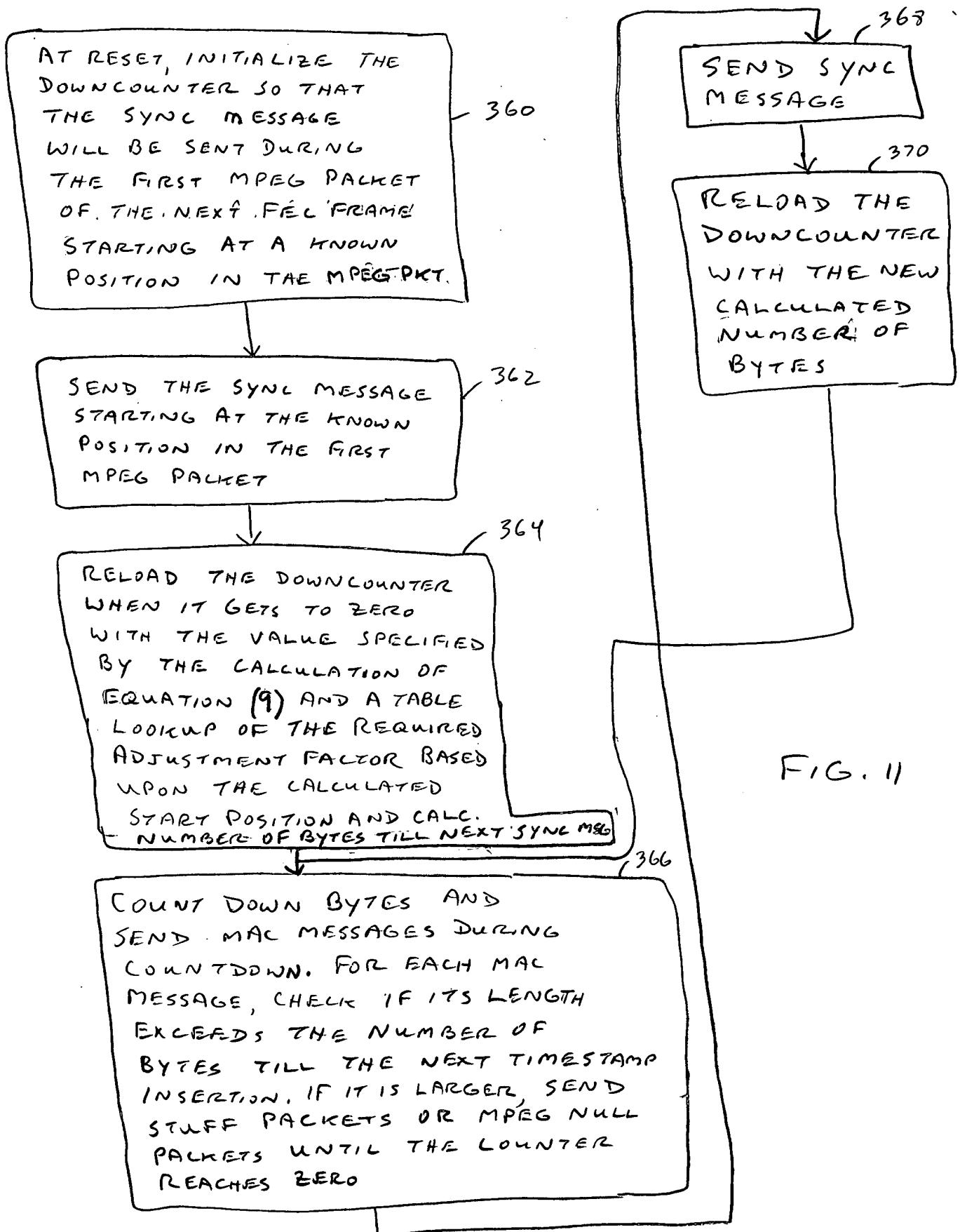
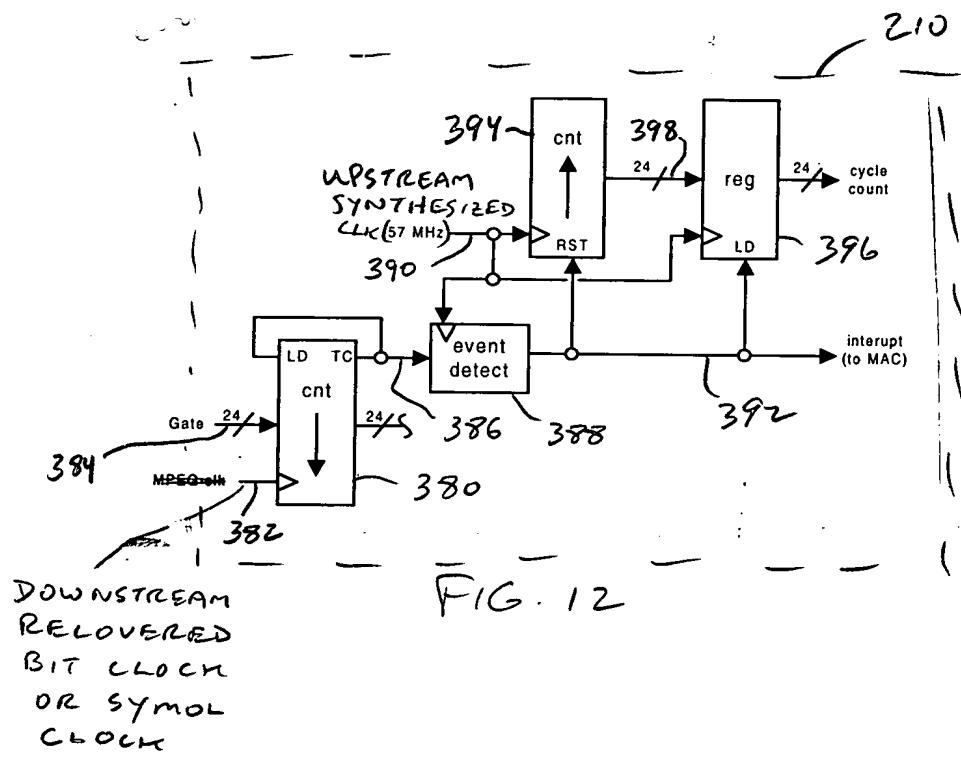
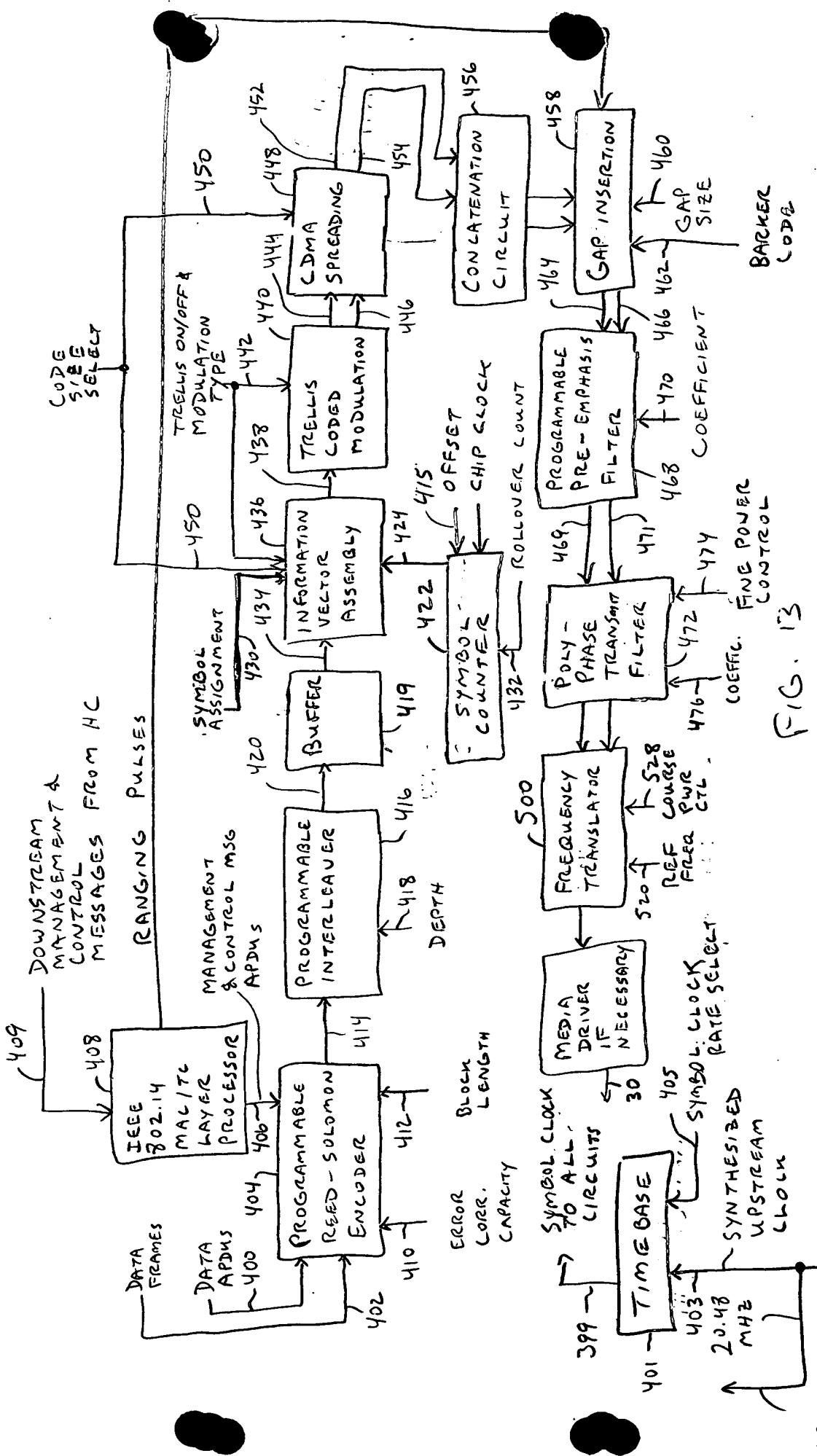


FIG. 11



090714036 - 0506598



CIRCUITS  
THAT NEED  
THE MANNER  
CLOCK TICK  
RATE

869050 " 9E042060

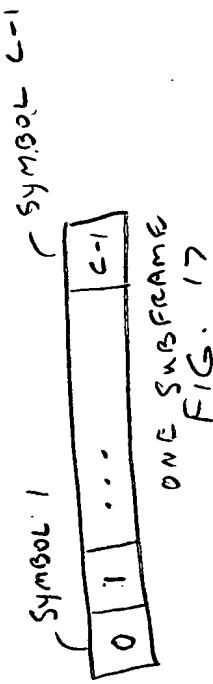
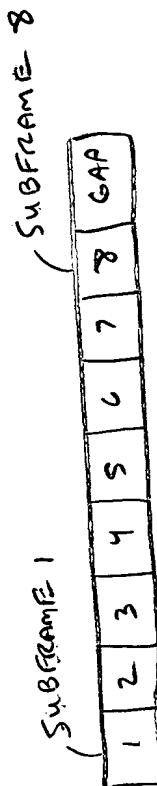
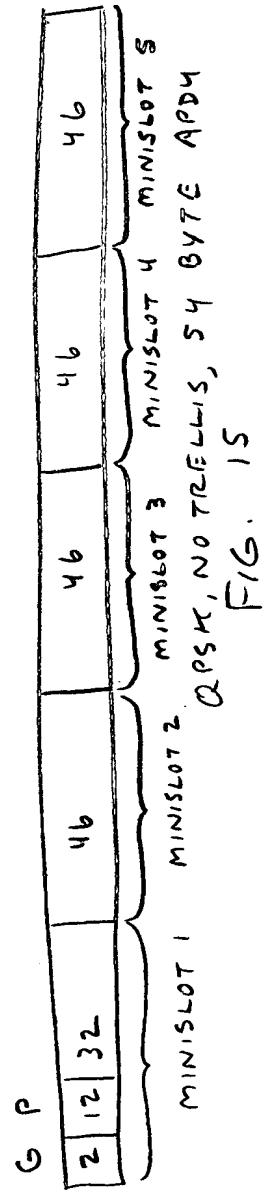
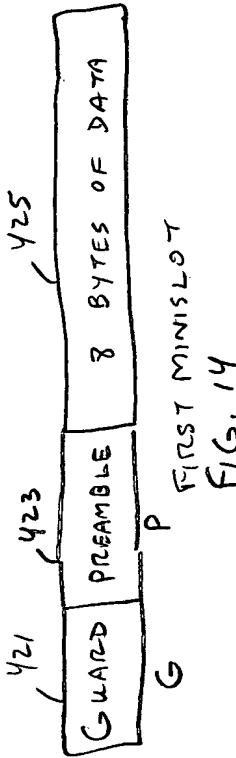


FIG. 18A: CREATE INFORMATION VECTOR = ONE SYMBOL

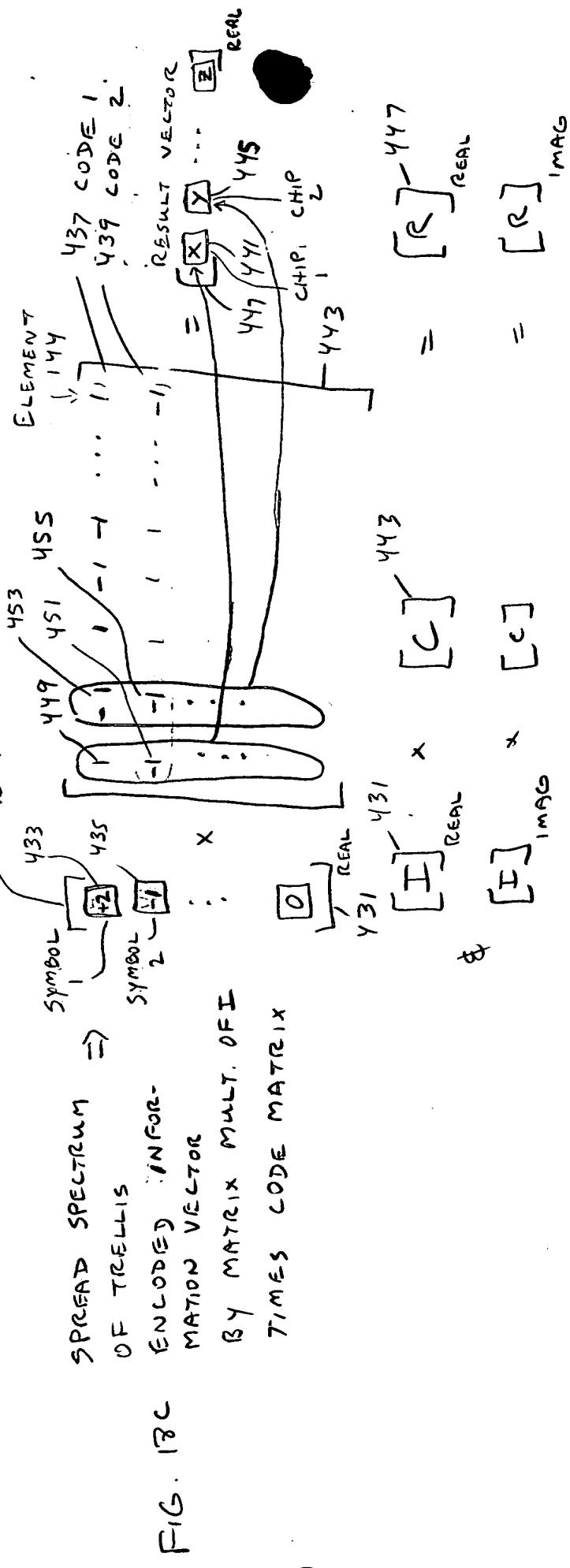
$y_{27} \xrightarrow{\text{ONE SUBFRAME}} \begin{bmatrix} 3 & 0 & \dots & 0 \end{bmatrix} = \mathbf{I}$

ONLY CODES 1 & 2 ASSIGNED FOR MINISLOT IN WHICH THIS SYMBOL WILL BE SENT.

FIG. 18B: TRELLIS ENCODE INFORMATION VECTOR = ONE SYMBOL

$y_{33} \xrightarrow{\text{REAL PART OF I}} \begin{bmatrix} 1 & 0 & \dots & 0 \end{bmatrix} = \mathbf{I}$

Symbol Symbol



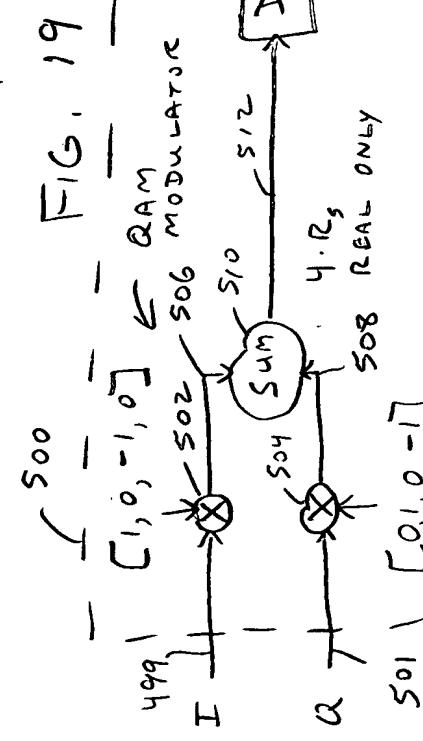
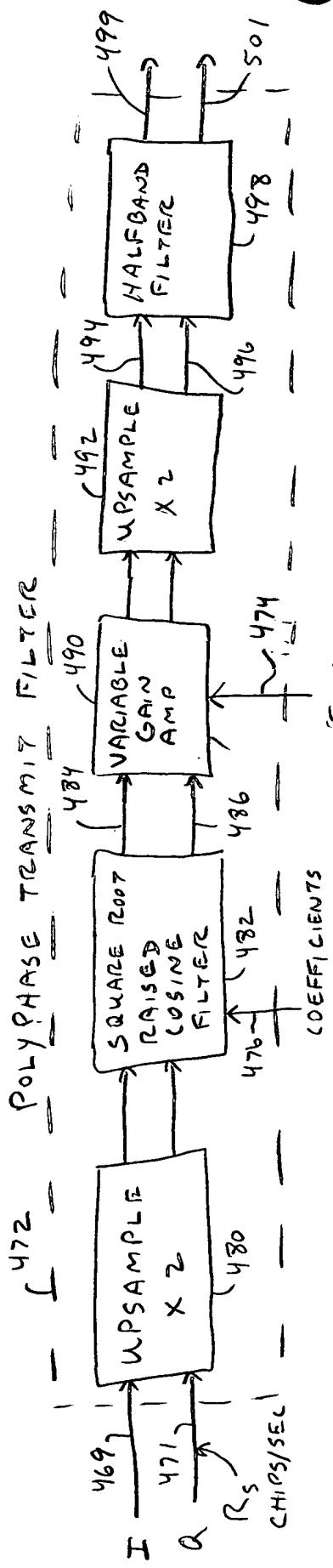


FIG. 19

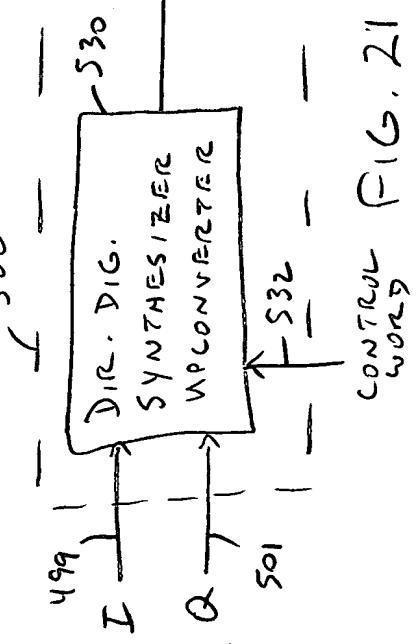
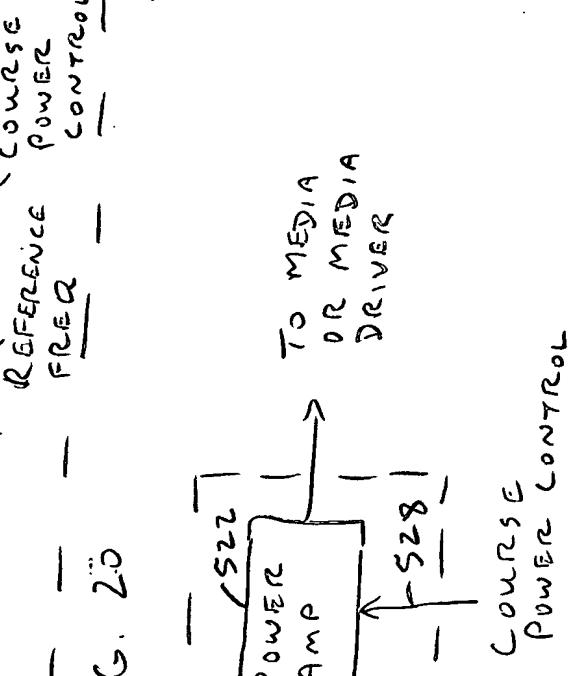


FIG. 20



control FIG. 21

Course Power Control

916 9050 " 9E 04 2050

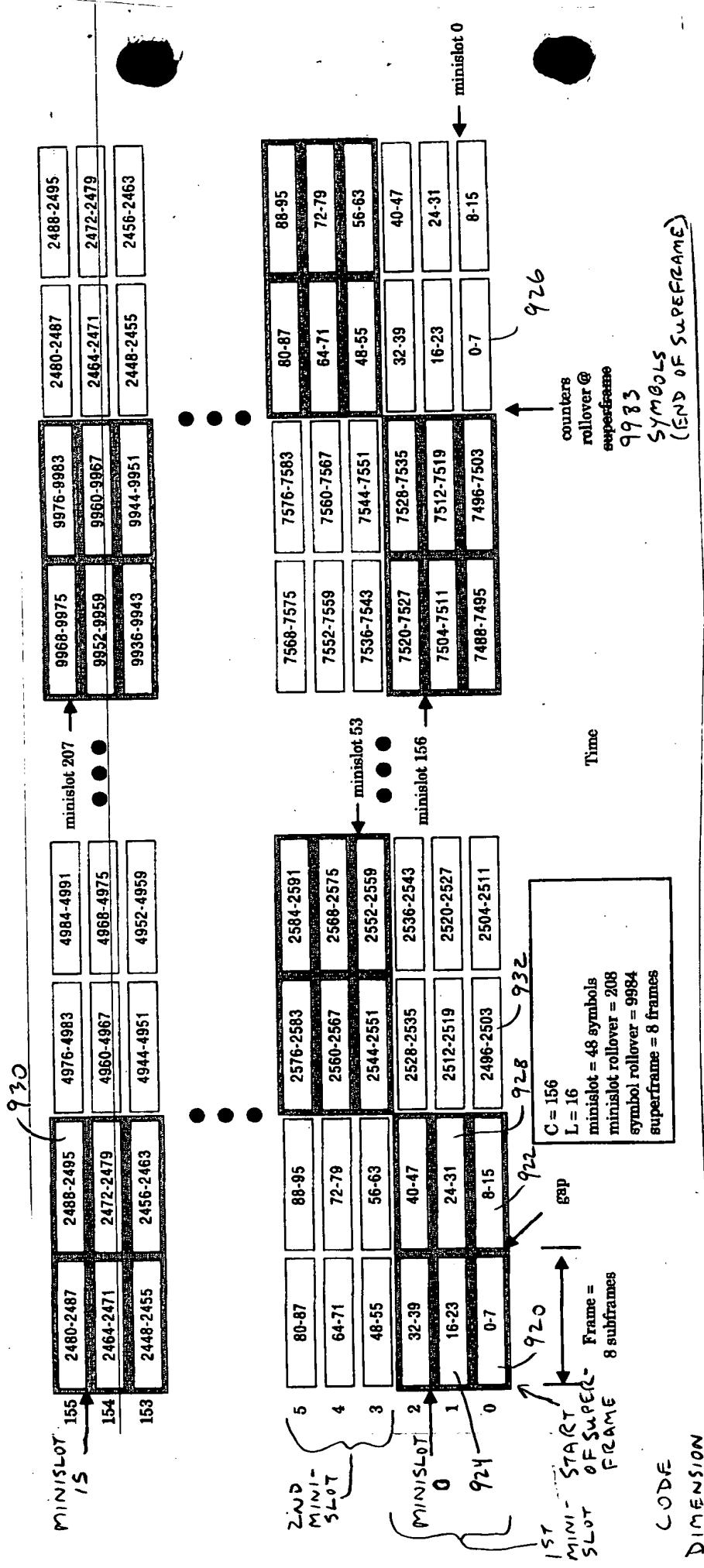


FIG. 22

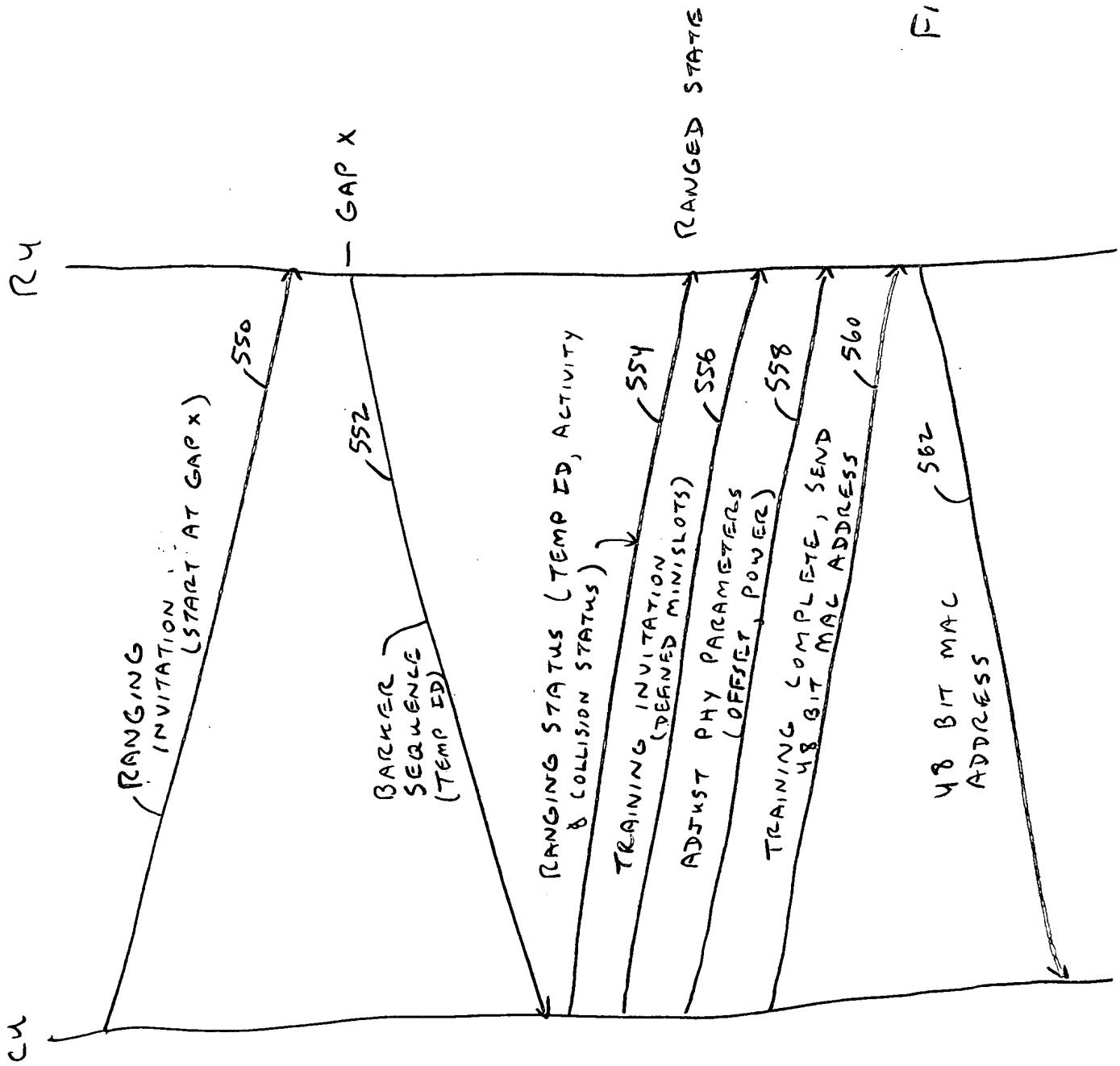


FIG. 23

09040360060606

09074035 - 050698

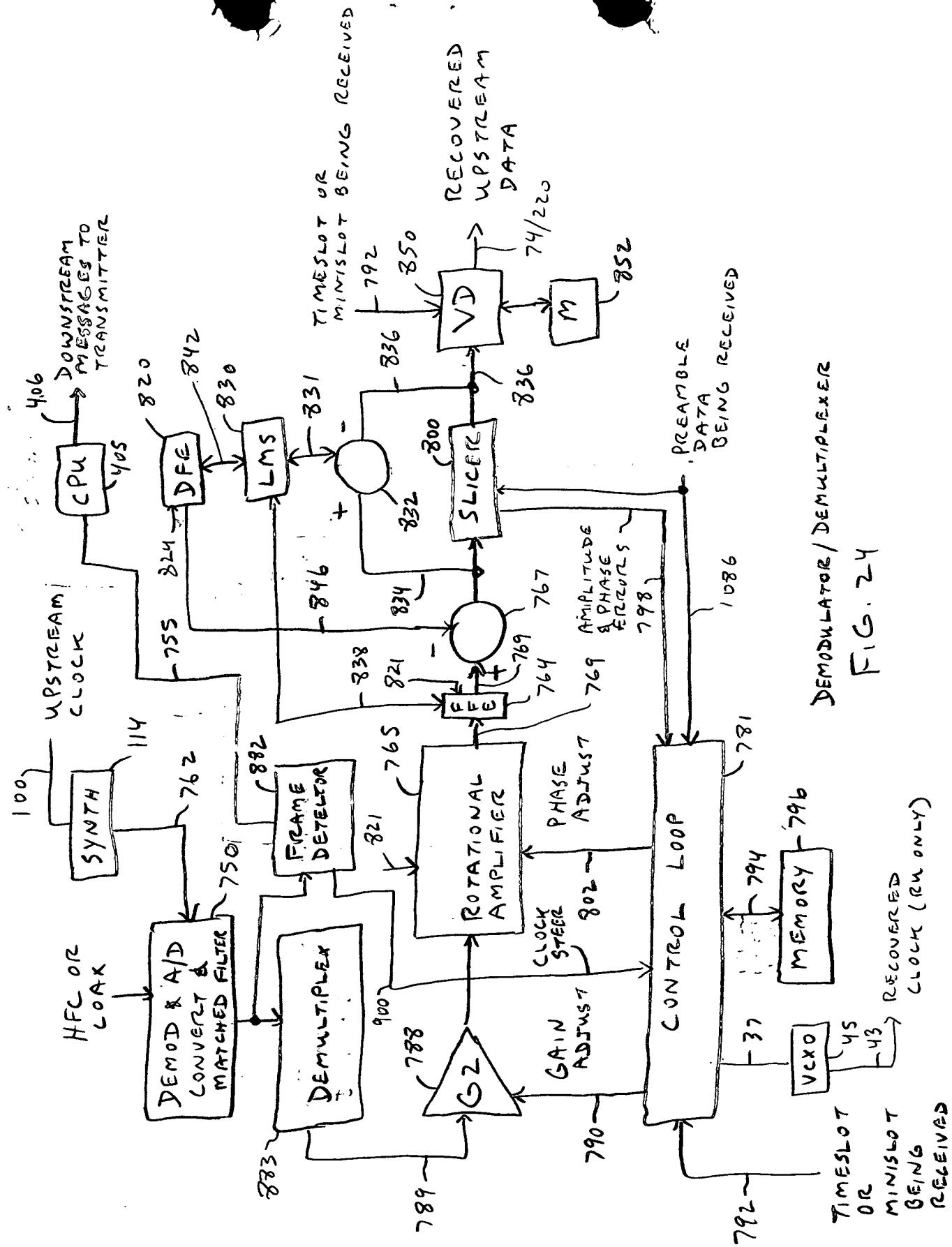


FIG. 24